

# SMT.ML: A Multi-Backend Frontend for SMT Solvers in OCaml

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**Abstract.** SMT solvers are essential for applications in artificial intelligence, software verification, and optimisation. However, no single solver excels across all formula types, and different applications may require the use of different solvers. While the SMT-LIB language enables multi-solver support, it also incurs heavy I/O overhead. To address this, we introduce SMT.ML, an SMT-solver frontend for OCaml that simplifies integration with various solvers through a consistent interface. Its parametric encoding facilitates the easy addition of new solver backends, while optimisations like formula simplification, result caching, and detailed error feedback enhance performance and usability. Furthermore, SMT.ML is the only SMT frontend that includes a simplification-management engine for streamlining the integration of new formula simplifications and the verification of their correctness. Our evaluation demonstrates that SMT.ML’s results are consistent with those of its backend solvers and that its optimisations are highly effective on formulas generated from the symbolic execution of an extensive program-analysis benchmark.

**Keywords:** SMT Solvers · Symbolic Execution · OCaml · SMT-LIB

## 1 Introduction

Since their emergence in the early 2000s, SMT solvers have become increasingly relevant and are now fundamental to numerous applications in modern life. They are applied in various scientific and industrial domains, ranging from planning problems in artificial intelligence [15] to software verification and test generation in software engineering [7, 16, 30, 41], and even the optimisation of production chains in operations research [11].

While there are now multiple industry-strength SMT solvers for one to choose from, none of them is perfect for all applications. For example, recent SMT-COMP [14] results reveal considerable differences in solver ranking across various theories. Hence, even within a single application, there may be benefits to using multiple solvers for tackling different types of formulas. However, switching between solvers often entails costly and error-prone integration work.

A common approach to interfacing with multiple solvers is to use SMT-LIB [9], a solver-agnostic textual format supported by almost all leading SMT solvers. In this approach, the given formula is serialised into an SMT-LIB formula and then the generated file is passed to the most appropriate solver for analysis. This textual interface, however, introduces substantial I/O overhead, making it impractical for performance-critical applications such as symbolic execution, program analysis, or synthesis engines. For efficiency, it is therefore essential for solvers to be integrated into the codebase as external libraries, linked directly via their native APIs. Unfortunately, these APIs differ widely in, for example, naming conventions, data representations, and type safety, making them challenging to understand and use in a uniform way.

While shared APIs for multiple solvers exist for some languages, such as Python and C++, they lack mechanisms to verify the correctness of any internal simplifications and do not address performance bottlenecks caused by repeated or redundant solver queries. Moreover, the OCaml ecosystem, which is home to a number of program-analysis and verification tools, has, until now, lacked a unified frontend for integrating multiple SMT solvers efficiently and safely.

To address these challenges, we introduce SMT.ML, a new SMT solver frontend for OCaml. SMT.ML simplifies the integration of OCaml programs with multiple SMT solvers by providing a consistent SMT-LIB-compatible language connected to five state-of-the-art solver backends: Alt-Ergo [22], Bitwuzla [50], Colibri2 [12], cvc5 [6], and Z3 [26]. With SMT.ML, OCaml developers do not need to understand any intricate detail of the API of these solvers to use them; they only have to create an SMT.ML formula and select the desired solver backend. Importantly, as part of the SMT.ML development effort, we created, for the first time, OCaml APIs for two SMT solvers: Colibri2 [12] and cvc5 [6].

The key novelties of SMT.ML when compared to other SMT frontends are:

1. a new *parametric encoding* that relies on a common solver API to translate SMT.ML formulas into the native logic of each backend solver (§3);
2. a *simplification-management system* that allows developers to specify simplification rules using a new declarative domain-specific language (DSL), from which both their OCaml implementations and corresponding Lean [49] proof skeletons are automatically generated (§4.1);
3. a *caching system* for satisfiability results, which normalises SMT queries to maximise cache hits and avoid redundant computation (§4.2).

To the best of our knowledge, SMT.ML is the first SMT frontend to have any of these features. The parametric encoding streamlines addition of new solvers to SMT.ML by requiring the developer to implement only a small, uniform set of functions, avoiding code duplication. The simplification management and caching systems are both solver-agnostic and together lead to a substantial increase in SMT performance on realistic program-analysis workloads. In addition, the former enables systematic verification of simplification correctness and provides a framework for extending the system with new verified simplifications.

We perform a comprehensive evaluation of SMT.ML on approximately 206K formulas from the official SMT-LIB benchmark [56] and 2.3M formulas obtained

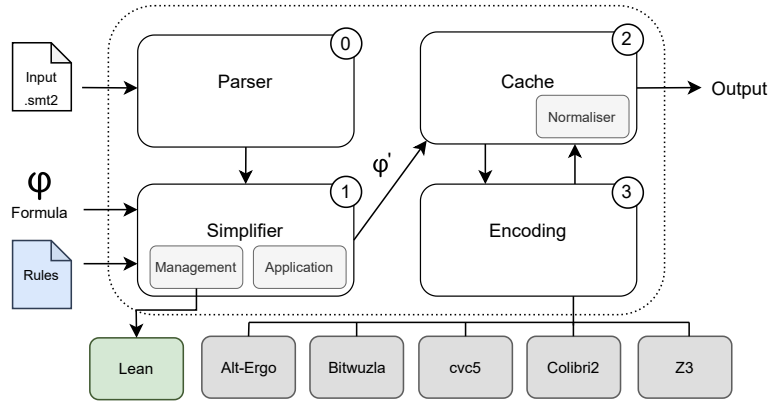


Fig. 1: Overview: Architecture of SMT.ML.

from symbolically executing the Test-Comp 2023 dataset [10] (§5). The results show that the behaviour of SMT.ML is fully consistent with the behaviour of the supported solvers, that the overhead of SMT.ML is negligible w.r.t. overall solving time (below 1% on average), and that the simplifications and caching of SMT.ML yield up to a 1.6x speed-up on formulas produced by symbolic execution.

We have made SMT.ML fully accessible to the OCaml and research communities. It is actively used in various research projects across both academia [47] and industry [2], and has been integrated into OPAM [63], the OCaml package manager, simplifying its incorporation into future OCaml projects.

## 2 Architecture

Figure 1 presents an overview of the architecture of SMT.ML. While SMT.ML was primarily conceived to be used as a library within an OCaml application, it could, in principle, also be used as a standalone tool. Therefore, it accepts inputs either in the form of native SMT.ML formulas or SMT-LIB [9] textual formulas. In the case of the latter, the SMT-LIB formula is first parsed by the *Parser module (Step 0)* and converted into an SMT.ML native formula. Given a native formula, SMT.ML performs the following steps:

- *Simplifier module (Step 1)*: This module applies a range of transformations to the given formula to reduce its complexity while preserving its original semantics. For instance, it applies the following algebraic identity to simplify bit-vector formulas:

$$\text{concat}(\text{extract}(x, h, m), \text{extract}(x, m, l)) \rightarrow \text{extract}(x, h, l)$$

where the `concat` operator concatenates the two given bit-vectors and the `extract` operator returns the slice of the given bit-vector corresponding to the specified bounds, padding with additional zeros if necessary.

- *Cache module (Step 2)*: This module normalises the given formula, checks if its satisfiability was already computed, and if so, returns the stored result. As part of the normalisation process, we rename symbolic variables in a standardised way to maximise cache hits.
- *Encoding module (Step 3)*: This module encodes SMT.ML expressions using the native OCaml bindings of the selected solver. It is parametric on a Core Solver API, simplifying the addition of new solver backends.

The simplifier module is also responsible for managing simplification rules, both converting them into the executable OCaml code that applies them and generating the Lean proof skeletons that, once completed, establish their correctness.

## 2.1 Why use SMT.ML?

In this section, we discuss in more detail the three main advantages that SMT.ML introduces for developers working with SMT solvers in OCaml.

*Solver Independence.* A key advantage of SMT.ML is its ability to interface with multiple SMT solvers through a single solver-independent frontend. In doing so, SMT.ML eliminates the need for developers to tailor their code to a specific solver API and lets them transparently switch between solvers, selecting whichever one is best for a given problem. This decision can even be made at runtime, allowing for the application of customised portfolio strategies [62].

*Performance Optimisations.* Interactions with SMT solvers are computationally expensive and can become a bottleneck in client applications. For instance, these interactions are known to be one of the main performance degradation factors in symbolic execution tools [47]. Our evaluation, described in detail in §5, shows that SMT.ML, with its solver-agnostic formula simplifications and caching of satisfiability results, can introduce substantial performance improvements when compared to using any single SMT solver directly.

*Usability.* OCaml bindings for SMT solvers often provide few type safety guarantees, with many using one generic OCaml type to represent SMT expressions denoting different types of values, such as integers or strings. For instance, OCaml bindings for Z3 [26] use a single type for all general expressions regardless of their underlying sort. This leads to ill-typed expressions not being detected at

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### Listing 1 Type violation of a function declaration using Z3 OCaml bindings.

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```

1 (* Function Declaration: foo : int -> int *)
2 let foo = FuncDecl.mk_func_decl_s ctx "foo" [ int_sort ] int_sort in
3
4 let str_sort = Seq.mk_string_sort ctx in
5 let value = Symbol.mk_string ctx "value" in
6 let str_const = Expr.mk_const ctx value str_sort in
7
8 let foo_app = Expr.mk_app ctx foo [ str_const ] in
9 (* foo(value) + 2 >= 2 *)
10 let formula = Arithmetic.mk_ge ctx (Arithmetic.mk_add ctx [ foo_app ; two ]) two

```

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|                               |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
|-------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| TYPES                         |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| $t ::=$                       | <code>Tunit</code>   <code>Tbool</code>   <code>Tint</code>   <code>Treal</code>   <code>Tbitv int</code>   <code>Tfp int</code>   <code>Tstr</code>   <code>Tregexp</code>   <code>Tapp</code>   <code>Tlist</code>                                                                                                                                                                                                                                                                                                                                                                                            |
| VALUES                        |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| $v \in \mathcal{V}_{smt} ::=$ | <code>unit</code>   <code>true</code>   <code>false</code>   <code>int</code>   <code>real</code>   <code>i8</code>   <code>i32</code>   <code>i64</code>   <code>f32</code>   <code>f64</code>   <code>str</code>   <code>regexp</code>   <code>list v</code>                                                                                                                                                                                                                                                                                                                                                  |
| EXPRESSIONS                   |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| $e \in \mathcal{E}_{smt} ::=$ | <code>v</code>   <code>x<sub>t</sub></code>   <code>unop</code> ( <code>op</code> , <code>t</code> , <code>e</code> )   <code>binop</code> ( <code>op</code> , <code>t</code> , <code>e</code> , <code>e</code> )   <code>triop</code> ( <code>op</code> , <code>t</code> , <code>e</code> , <code>e</code> , <code>e</code> )<br>  <code>relop</code> ( <code>op</code> , <code>t</code> , <code>e</code> , <code>e</code> )   <code>cvtop</code> ( <code>op</code> , <code>t</code> , <code>e</code> )   <code>naryop</code> ( <code>op</code> , <code>t</code> , <code>list e</code> )   <code>list e</code> |
| COMMANDS                      |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 |
| $c \in \mathcal{C}_{smt} ::=$ | <code>declare</code> ( <code>x<sub>t</sub></code> )   <code>assert</code> <code>e</code>   <code>check_sat</code> ( <code>list e</code> )   <code>get_model</code><br>  <code>get_value</code> <code>e</code>   <code>pop int</code>   <code>push int</code>   <code>reset</code>   <code>exit</code>                                                                                                                                                                                                                                                                                                           |

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Fig. 2: The syntax of SMT.ML.

compile-time leading to hard-to-debug runtime errors. Listing 1 illustrates this issue by applying an uninterpreted function that expects an integer argument (line 2) to a string constant (line 10), resulting in a runtime error saying that the expected and the actual argument types do not match. In contrast, SMT.ML expressions are well-typed by construction, avoiding this class of bugs and promoting code correctness and reliability in the development process.

## 2.2 Syntax of SMT.ML

The syntax of SMT.ML is presented in Figure 2. There are two main syntactic categories: expressions, which denote values, and commands, which represent instructions given to the solver. SMT.ML currently supports the theories of quantifier-free linear integer and real arithmetic (QF\_LIA and QF\_LRA), bit-vectors (QF\_BV), floating-point arithmetic (QF\_FP), and strings (QF\_S). These are the theories most commonly required for software verification and analysis tasks [5, 17], which is the main application of SMT.ML. However, SMT.ML has a modular and extensible architecture, making it easy to add support for new theories.

*Values.* SMT.ML values,  $v \in \mathcal{V}_{smt}$ , include the unit value, booleans (`true` and `false`), integers, reals, machine integers (8, 32 and 64-bit), IEEE 754 floating-point numbers (32 and 64-bit) [38], strings, regular expressions, and lists.

*Expressions.* Expressions,  $e \in \mathcal{E}_{smt}$ , consist of: values; typed symbolic variables  $x_t$ , where  $x$  denotes the variable identifier and  $t$  its type; and operators, which can be unary (`unop`, such as logical negation), binary (`binop`, such as addition), ternary (`binop`, such as bit-vector slicing), n-ary (`naryop`, such as list concatenation, relational (`relop`, such as comparisons), or conversion-related (`cvtop`, such as casting from an integer to a string). Importantly, expression constructors enforce well-typedness by explicitly carrying the expression type.

*Commands.* SMT.ML provides the developer with a set of commands for interacting with SMT solvers. In particular, one can: declare a symbolic variable  $x$  of a given type  $t$ ; assert that a given Boolean expression (i.e., formula) holds; check satisfiability of the current solver state additionally assuming a given list

of formulas; get a model for the last satisfiability check; get a model for a given expression in the context of the last satisfiability check; introduce/remove a number of assertion levels; and reset/terminate the interaction with the solver.

### 3 Encoding

The Encoding Module is the core component of SMT.ML that is responsible for translating native SMT.ML expressions and commands into the expressions and commands of the solver backends. Instead of having a separate encoding process for each backend, we identify a set of fundamental functionalities required for solver interaction and bring them together to form a parametric *Core Solver API*, which we then use to define a solver-independent encoding. For a solver to be integrated into SMT.ML, all that it needs to do is: support integration with OCaml via native bindings; implement our Core Solver API as a wrapper around these bindings; and plug this implementation into SMT.ML. This parametric approach, which is illustrated in Figure 3, substantially streamlines the addition of new solver backends to SMT.ML and avoids code duplication.

Importantly, extending a given solver with the wrapper code that implements the Core Solver API is significantly easier than implementing a solver-specific encoding from scratch. In particular, as nearly all required functions are typically present in native solver APIs, implementing them only amounts to mapping their names to those expected by the Core Solver API.

*Solver-Independent API.* The Core Solver API, denoted by  $\mathcal{S}$ , encompasses all functions on solver values, expressions, and commands on which the encoding of SMT.ML expressions and commands depends. These functions can be divided into the following four main categories:

- *Values:* these functions are responsible for mapping SMT.ML primitive values,  $v \in \mathcal{V}_{smt}$ , into values from the corresponding target solver;
- *Operators:* these functions map SMT.ML operators to the corresponding solver operators: for example, solver wrappers are expected to implement the addition operator, which, when given two solver expressions, returns a target solver expression representing their sum;

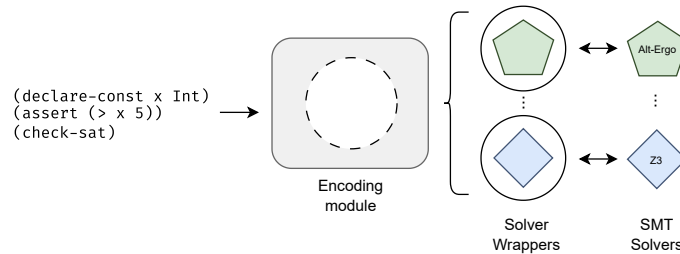


Fig. 3: The parametric Encoding module of SMT.ML.

$$\begin{array}{c}
\text{VALUES} \\
\frac{\mathcal{S}.val(v) = v'}{T_{\mathcal{S}}(v) = v'} \\
\\
\text{SYMBOLS} \\
\frac{\mathcal{S}.symbol(s) = s'}{T_{\mathcal{S}}(s) = s'} \\
\\
\text{UNARY OPERATORS} \\
\frac{T_{\mathcal{S}}(e) = te \quad \mathcal{S}.unop(uop, te) = e'}{T_{\mathcal{S}}(uop\ e) = e'} \\
\\
\text{N-ARY OPERATORS} \\
\frac{T_{\mathcal{S}}(e_i) = e'_i \mid_{i=1}^n \quad \mathcal{S}.naryop(nop, [e'_1, \dots, e'_n]) = e'}{T_{\mathcal{S}}(nop\ [e_1, \dots, e_n]) = e'}
\end{array}$$

Fig. 4: Parametric translation rules for SMT.ML (excerpt).

- *Commands*: these functions map SMT.ML commands,  $c \in \mathcal{C}_{smt}$ , into commands that manipulate and interact with the target solver; and
- *Lifting*: these functions map solver values back to SMT.ML values, and are essential when performing model extraction, as they allow models to be constructed using native SMT.ML values.

*Parametric Translation.* Using the API described above, we implement a generic translation from SMT.ML constructs to the corresponding solver-specific constructs. Expression translation is formalised as a function  $T_{\mathcal{S}} : \mathcal{E}_{smt} \rightarrow \mathcal{S}.Expr$  that receives an SMT.ML expression  $e \in \mathcal{E}_{smt}$  and generates a target solver expression  $te \in \mathcal{S}.Expr$ . An excerpt of the translation rules is shown in Figure 4.

Values  $v$  and symbols  $s$  are translated into their counterparts in the target solver  $\mathcal{S}$ , using the functions  $\mathcal{S}.val(v)$  and  $\mathcal{S}.symbol(s)$ , respectively. Unary operators,  $uop\ e$ , are translated by applying the solver’s unary operator to the translated argument using the Core Solver API function  $\mathcal{S}.unop(uop, te)$ , which produces the solver expression denoting  $uop$  applied to  $te$ . The translation proceeds similarly for all other kinds of operators.

SMT.ML supports five backend solvers: Alt-Ergo, Bitwuzla, Colibri2, cvc5, and Z3. For each of these solvers we have implemented the Core Solver API. Further, for Colibri2 we needed to implement a user-facing API that allows for solver interaction. Finally, for cvc5 we had to implement OCaml bindings from scratch [44], as it originally had none. In this way, we allow for cvc5 to be natively integrated not only into SMT.ML but also into any other OCaml-based tool.

## 4 Backend-independent Optimisations

The usefulness of SMT.ML extends beyond its capability to interact with multiple SMT solvers through a unified syntax. A significant aspect of its design is the inclusion of backend-independent optimisations that enhance performance when checking satisfiability. Here, we discuss the two most important such optimisations: expression simplifications (§4.1) and caching (§4.2).

### 4.1 Expression Simplifications

In applications that interact with SMT solvers, the size and complexity of the problem at hand can significantly affect solver efficiency, making performance

the primary bottleneck [66]. To address this, SMT.ML tries to reduce expression complexity by applying a set of semantics-preserving simplifications before these expressions are passed to a solver. Currently, SMT.ML comes with 42 simplification rules, spanning the theories of bit-vectors (11 rules), strings (3 rules), boolean (2 rules), and 26 generic rules that applicable to multiple theories.

Importantly, instead of hardcoding these simplifications directly into the OCaml codebase, we design a simple domain-specific language (DSL) that enables their declarative specification. This allows users of SMT.ML to easily examine, add, or remove simplifications by need. From these specifications, we automatically generate the corresponding OCaml implementations and, if applicable, Lean [49] proof skeletons, allowing users to formally prove simplification correctness by hand, thereby reducing the trusted computing base of SMT.ML. To our knowledge, SMT.ML is the first SMT frontend to adopt this approach.

*Simplification management.* SMT.ML simplification rules,  $r \in \mathcal{R}$ , are of the form  $e_1 \Rightarrow e_2$  **when**  $e_f$ , meaning that if the boolean expression  $e_f$  holds, then the expression  $e_1$  can be rewritten to  $e_2$ . For instance, consider the simplification rule

$$\text{Extract}(x, h, l) \Rightarrow x \text{ when } (l \geq 0 \wedge h < |x| \wedge h - l + 1 = |x|) \quad (1)$$

where **Extract** is an SMT.ML operator that returns the slice of a given bit-vector  $x$  from bit  $l$  to bit  $h$  inclusive, padding with zeros if the slice falls outside  $x$ . This rule says that **Extract**( $x, h, l$ ) can be rewritten to  $x$  when  $l$  is non-negative,  $h$  is within the bounds of  $x$  and  $h - l + 1$  equals the size of  $x$ . From this rule, SMT.ML automatically generates the OCaml code that implements it (Fig. 5, left), as well as the corresponding Lean proof skeleton (Fig. 5, right).

|                                                                                                                                                                                                                                                                                               |                                                                                                                                                                                                                       |
|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <pre> 1 let simplify_trip ty op hte1 hte2 hte3 = 2   match op, hte1, hte2, hte3 with 3     ... (* other simplification cases *) 4     Extract, bv, Val (Int h), Val (Int l) 5     when l &gt;= 0 6     and h &lt; Ty.size (ty bv) 7     and h - l + 1 = Ty.size (ty bv) 8     -&gt; bv </pre> | <pre> 1 lemma simplification_trip_000004 2   (w : Nat) 3   (h1 : Int) 4   (n : BitVec w) 5   (h0 : l &gt;= 0) 6   (h1 : h &lt; w) 7   (h2 : (w : Int) = h - l + 1) : 8   BitVec.extractLsb h l n = n := by ... </pre> |
|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|

Fig. 5: Example: generated OCaml code (left) and Lean proof skeleton (right).

*Simplification application.* At runtime, SMT.ML takes the formulas that are to be checked for satisfiability and, for each such formula, keeps looping over the simplification rules, applying those whose constraints are satisfied until no further rules can be applied; this process is illustrated in Algorithm 1. We note that it is up to the writer of the simplifications to ensure that their collective application does not result in infinite loops, such as  $e \xrightarrow{r_1} e' \xrightarrow{r_2} e$ , where  $e$  is the original expression,  $e'$  the simplified expression, and  $r_1$  and  $r_2$  two simplification rules.

To apply a simplification rule  $e_1 \Rightarrow e_2$  **when**  $e_f$  to a given expression  $e$  in execution context  $e'_f$ , SMT.ML proceeds as follows:

1. find a substitution  $\theta$  such that  $\theta(e_1) = e$ ;
2. if successful, check that  $e'_f \models \theta(e_f)$ ;

**Algorithm 1** Expression simplification algorithm.

---

```

1: procedure SIMPLIFY( $e$ )
2:    $e' \leftarrow e$ 
3:   for all  $r \in \text{Rules}$  do
4:      $e' \leftarrow \text{apply } r \text{ to } e'$ 
5:   if  $e' \neq e$  then
6:     return SIMPLIFY( $e'$ )
7:   else
8:     return  $e'$ 

```

---

3. if successful, replace  $e$  with  $\theta(e_2)$ .

For instance, in execution context  $\pi \equiv |z| = 2$ , applying the simplification from Equation 1 to the expression `extract(concat( $y, z$ ),  $|y| + 2, 0$ )` yields the expression `concat( $y, z$ )` with substitution  $\theta = [x \mapsto \text{concat}(y, z), l \mapsto 0, h \mapsto |y| + 2]$

*Lean correctness proofs.* We generate Lean proof skeletons for 31 out of the 42 simplifications and provide the corresponding proofs. As all of our simplifications describe basic properties of the underlying datatypes, their proofs are very simple and highly automated, relying on the comprehensive mathematical library of Lean. The remaining 11 simplifications are purely *definitional*, in that they describe the behaviour of SMT.ML operators on concrete inputs in terms of the corresponding OCaml operators. One such simplification, for example, is:

$$\text{Length}(l) \Rightarrow \text{List.length } l \text{ **when** concrete}(l) \quad (2)$$

which declares that the SMT.ML `Length` operator coincides with the OCaml `List.length` operator. Note that this simplification uses the `concrete( $x$ )` predicate in the **when** clause, which holds if and only if  $x$  is concrete.

## 4.2 Caching

Caching of intermediate satisfiability results is a standard technique used in SMT solvers and solver clients to improve performance [53, 57]. However, it is not common for identical formulas to be queried multiple times, even in applications that make an intensive use of SMT solvers. To address this, formula caching systems [64] typically implement normalisation strategies [36] with the goal of maximising cache hits. SMT.ML comes with its own formula caching system equipped with a normalisation procedure that performs:

- *Standardisation of associative operators:* a standard order is imposed on expressions that include such operators. For instance, considering the disjunction operator,  $\vee$ , we have that  $(x \vee y) \vee z = x \vee (y \vee z)$ . In SMT.ML, expressions that include chained associative operators are always rewritten to ensure that the leftmost operations are performed first.
- *Variable renaming:* variables are renamed to ensure structurally identical formulas with different variable names are considered equal.

In addition to minimising the number of queries, one can also enhance the performance of solver clients by reducing the number of expressions created at runtime. In fact, solver clients often generate a large number of expressions, frequently with repeated elements. As the number of queries grows memory consumption increases, impacting client performance; a prime example of this are symbolic execution engines [18]. The standard approach to reducing the memory impact of such systems is *hash-consing* [29], a technique that ensures that no two physical copies of the same expression are ever created by storing expressions in a hash table. SMT.ML includes a hash-consing module that prevents duplication of identical expressions. To this end, whenever an SMT.ML expression constructor is called, it checks whether the expression already exists, and, if it does, returns the previously stored expression.

Listing 2 illustrates this process for the `Or` constructor. We define the `mk_or` hash-consing constructor, which builds a boolean disjunction of two hash-consed expressions. In line 3, we construct the binary expression, and in line 4, we attempt to retrieve a previously constructed expression from the hash-consing table. If the expression is not found, we add it to the table and return the value constructed in line 3. One might notice that in line 3 we allocate memory to construct an expression, only to later use an existing one retrieved from the hash-consing table. However, as OCaml initially allocates values in the minor heap using a bump allocator [48], this allocation incurs no cost. Additionally, OCaml will collect the temporarily allocated values during minor garbage collection.

---

**Listing 2** Hash-consing constructor for boolean disjunction.

---

```

1 let table = Hashtbl.create 251
2 let mk_or hte1 hte2 =
3   let x = Binary (Or, hte1, hte2) in
4   try Hashtbl.find table x with Not_found -> Hashtbl.add table x x; x

```

---

## 5 Evaluation

We evaluate SMT.ML with respect to the four following questions:

- **EQ1:** Does SMT.ML exhibit behaviour consistent with the supported solvers?
- **EQ2:** How much does SMT.ML’s overhead impact overall performance?
- **EQ3:** How do SMT.ML’s optimisations impact its overall performance?
- **EQ4:** Are SMT.ML’s simplifications transparent and trustworthy?
- **EQ5:** How does SMT.ML compare to other SMT frontends?

All experiments were performed on a server with a 12-core Intel Xeon E5-2620 v4 CPU and 32GB of RAM, running Ubuntu 24.04.1 LTS. We compiled SMT.ML using the OCaml 5.3.0 compiler. For the SMT solvers, we used Alt-Ergo v2.6.2, Bitwuzla v0.8.0, Colibri2 development version (commit 1feba887), cvc5 v1.3.0, and Z3 v4.13.0. Each benchmark was run with a timeout of 60s and 10GB memory limit. The benchmarking code, reproducibility scripts, and diagram generation scripts are all available at <https://github.com/formalsec/smtml>.

## 5.1 Datasets

To assess the correctness (§5.2) and performance overhead (§5.3) of SMT.ML, we used a subset of the official SMT-LIB benchmark [56]. In particular, we used approximately 206K SMT formulas that comprise the (quantifier-free) theories of: linear integer arithmetic (QF\_LIA), floating-point arithmetic (QF\_FP), bit-vector arithmetic (QF\_BV), string theory (QF\_S), and string theory with linear integer arithmetic (QF\_SLIA). We chose these theories as they are the ones both most commonly used in practice [14] and well-supported by the SMT.ML backends.

To assess how the optimisations of SMT.ML impact performance (§5.4), we used a dataset of approximately 2.3M SMT formulas generated through symbolic execution of the following sub-categories from the `c/ReachSafety` category of the official Test-Comp 2023 benchmark suite [10]: Arrays, Bit-vectors, Heap, ProductLines, and Sequentialized. These categories provide a diverse set of formulas commonly encountered during symbolic execution of binary code. We used the Owi symbolic execution engine [2] as it does not implement its own formula caching system and includes only a small set of built-in optimisations, thereby allowing for a more trustworthy assessment of the true impact of SMT.ML.

Note that the formulas found in the SMT-LIB benchmarks by design share few commonalities and lack redundancies by design. This is the exact opposite of our intended use case for SMT.ML, which is integration with tools that interact multiple times with a solver during execution and generate similar formulas in doing so, such as symbolic execution tools. For this reason, we do not evaluate the optimisations of SMT.ML against the SMT-LIB benchmarks.

## 5.2 EQ1: Behaviour consistency

To assess the correctness of SMT.ML, we compared the results obtained when running SMT.ML and when running each supported solver directly on the targeted SMT-LIB benchmarks. This cross-validation is facilitated by the fact that most SMT-LIB benchmarks are annotated with their expected outcome.

*Results.* All results produced by SMT.ML matched those directly produced by the solvers and were aligned with the provided expected outcomes.

*Takeaway 1:* The behaviour of SMT.ML is fully consistent with the behaviour of its backend solvers.

## 5.3 EQ2: Performance overhead

To quantify the overhead introduced by SMT.ML, we measured the time spent in SMT.ML versus the time spent in the backend solvers on the SMT-LIB benchmarks. SMT.ML’s tasks include parsing SMT-LIB formulas, applying simplifications, and encoding formulas for each of the backend solver APIs. For each theory, we used the times obtained when using the solver that performed best on average for that theory. Table 1 reports the average time spent in SMT.ML and in the solvers for each theory, along with the corresponding percentages.

Table 1: Average times: SMT-LIB benchmarks: SMT.ML vs. backend solvers.

| Theory  | SMT.ML    |      | Backend solver check-sat |       |
|---------|-----------|------|--------------------------|-------|
|         | avg. (ms) | (%)  | avg. (ms)                | (%)   |
| QF_BV   | 33.90     | 0.97 | 3460.69                  | 99.03 |
| QF_FP   | 0.36      | 0.01 | 9794.61                  | 99.99 |
| QF_LIA  | 20.97     | 3.13 | 648.97                   | 96.87 |
| QF_S    | 0.93      | 0.38 | 238.27                   | 99.62 |
| QF_SLIA | 0.56      | 0.37 | 155.02                   | 99.63 |
| Total   | 11.09     | 0.50 | 2215.12                  | 99.50 |

*Results.* The obtained results show that the overhead of SMT.ML is minimal when compared to solver runtime, staying below 1% for all theories except QF\_LIA and rising to at most 3.13%, for QF\_LIA. We believe that this larger percentage is due to the corresponding formulas being larger, and thus their parsing times.

*Takeaway 2:* SMT.ML introduces only negligible overhead on top of the time spent in its respective backend solvers.

#### 5.4 EQ3: Performance impact on symbolic execution benchmarks

We evaluated the impact of the SMT.ML backend-independent optimisations through an ablation study using a dataset of SMT formulas generated by the Owi symbolic execution engine [2] while executing the Test-Comp 2023 benchmark suite [10]. We considered the following three configurations:

- **Raw:** no optimisations, formulas are sent directly to the backend solver.
- **Simplifications:** formulas are simplified before being sent to the solver.
- **Simplifications and caching:** simplified formulas are also cached to avoid redundant computation.

Figure 6 summarises the cumulative runtime for each configuration across the selected benchmark categories. We consider only cvc5 and Z3 as they support all of the theories required for the dataset at hand and are also the most performant.

*Results.* The results indicate that, on the whole, the backend-independent optimisations significantly improve performance. In particular, the total results for the considered dataset (Figure 6, top left), show that the two optimisations together yield a  $1.59\times$  speedup with Z3 and  $1.54\times$  with cvc5 relative to the Raw configuration baseline. When examining categories in isolation, we can see, for example, that for c/ReachSafety-Arrays and c/ReachSafety-Heap simplifications alone account for most of the performance improvement, whereas for c/ReachSafety-ProductLines and c/ReachSafety-Sequentialized it is the caching that dominantly speeds up the execution. In particular, for the latter category, caching exhibits  $1.42\times$  and  $1.31\times$  speedups over the Simplifications only

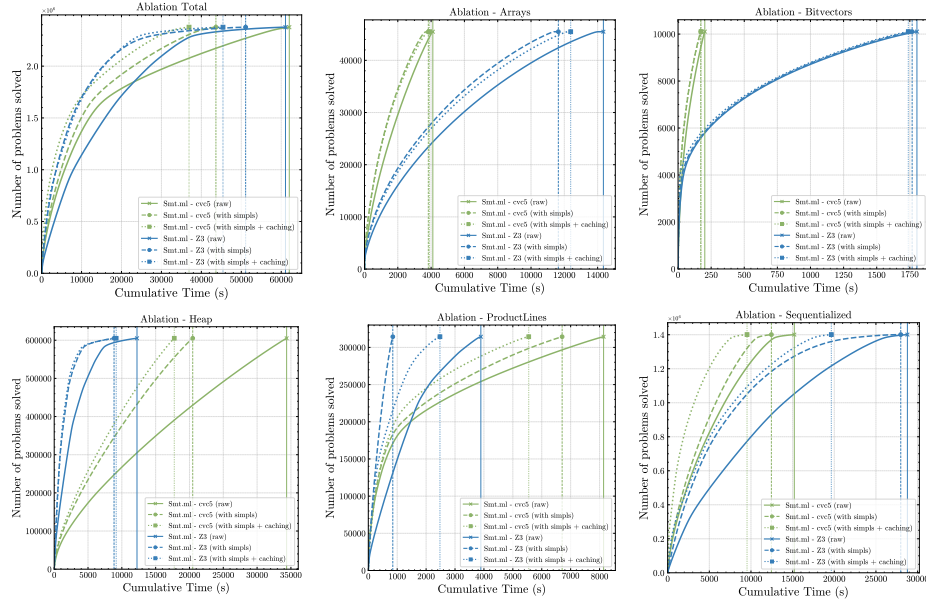


Fig. 6: Ablation study results by Test-Comp 2023 benchmark category.

configuration for Z3 and cvc5, respectively. These differences can be attributed to the nature of the formulas in each category: categories with many repeated sub-expressions benefit more from caching, whereas categories with complex but less repetitive formulas benefit primarily from simplifications.

*Takeaway 3:* The backend-independent optimisations significantly improve the performance of SMT.ML on symbolic execution benchmarks.

## 5.5 EQ4: Transparency and trustworthiness of simplifications

By developing a DSL for writing simplifications and a mechanism for automatic generation of corresponding OCaml code and Lean proof skeletons, we believe that we have provided users of SMT.ML with an easy-to-use simplification management system. When it comes to simplification correctness, on the one hand, the definitional simplifications only connect SMT.ML operators to basic OCaml data-structure operators in a way that if one trusts the latter, one should also trust the former. On the other hand, the correctness of all 31 non-definitional simplifications has been fully proven in Lean. At a higher level, we opted to prove simplification correctness in a proof assistant rather than using SMT as some simplifications, such as the involution of list reverse (e.g.,  $\text{Rev}(\text{Rev}(l)) \Rightarrow l$ ), require inductive reasoning.

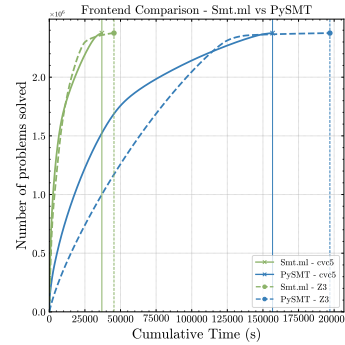
*Takeaway 4:* Users can easily examine and manage SMT.ML simplifications. All of these simplifications are trustworthy: most are formally verified in Lean, and the remaining ones rely on the correctness of fundamental OCaml operators.

## 5.6 EQ5: Comparison with other SMT frontends

We identified six relevant open-source SMT frontends: JavaSMT [4], PySMT [34], rsmt2 [19], SBV [28], Smt-Switch [46], and what4 [39]. We excluded JavaSMT, rsmt2, SBV, and what4 as they do not accept SMT-LIB input, making it impossible to measure their performance using standard benchmarks. We also excluded Smt-Switch, a state-of-the-art C++ frontend, because it throws an error when processing multiple formulas with the same solver instance if given two independent formulas in which there is a shared variable name. This behaviour forces the creation of a new solver instance for each query, preventing a fair comparison with SMT.ML, which reuses the same solver instance every time, and is, therefore, considerably faster. We are collaborating with the Smt-Switch team to resolve this<sup>4</sup> and will include a proper comparison in the final version of the paper.

Therefore, we compare SMT.ML against the only remaining frontend, PySMT [34]. We evaluated PySMT on the symbolic execution dataset described in §5.4, using cvc5 and Z3 as backend solvers, as they are the only two solvers supported by both SMT.ML and PySMT.

*Results.* The comparison between SMT.ML and PySMT is shown on the right-hand side. For both frontends, we used the best-performing configurations. The results show that SMT.ML substantially outperforms PySMT, with speedups of  $4.34\times$  and  $4.25\times$  with Z3 and cvc5, respectively. This can be partly attributed to the implementation languages: SMT.ML is implemented in OCaml, a compiled language, whereas PySMT is written in Python, an interpreted language that incurs additional overhead.



*Takeaway 5:* SMT.ML significantly outperforms the only comparable SMT frontend on symbolic execution benchmarks.

## 6 Related Work

*SMT Solvers.* SMT solvers have seen significant advancements since their emergence in the early 2000s [6, 8, 24, 26, 27], such as support for new theories, like strings [43] and quantified arithmetic [1], and new optimisation techniques, like new caching mechanisms [64] and portfolio strategies [55, 60]. As a result, they

<sup>4</sup> See <https://github.com/stanford-centaur/smt-switch/pull/437>.

are now used across the entire computer science community, with a wide variety of applications ranging from software verification and test generation [31, 40, 42] to combinatorial optimisation and classical operations research [11].

Currently, many SMT solvers are actively maintained, with 20 submitted to the 2024 edition of SMT-COMP [14]. Importantly, there is no one-size-fits-all solver: some excel in handling certain theories, others excel in handling others. Our solver selection for SMT.ML integration is guided by our specific needs. Initially, we supported Z3 [26] and Colibri2 [12], as they were already being used in our ongoing projects. Subsequently, we added support for cvc5 [6] and Bitwuzla [50], due to their excellent performance with bit-vectors and floating-point arithmetic, which are frequently required in symbolic execution. Most recently, we have also interfaced with Alt-Ergo [22], because it is the only SMT solver implemented entirely in OCaml. This makes it particularly attractive for our ecosystem, as it enables applications depending on Alt-Ergo to be compiled with `js_of_ocaml` [65], allowing native execution directly within the browser.

*Frontends for SMT Solvers.* Frontends [4, 19, 28, 34, 39, 46] play an essential role in making SMT solvers more accessible to the broader computer science audience. These interfaces often come with user-friendly input languages that are both more expressive and closer to real-world problem domains than the logics of existing solvers, streamlining user interaction. Frontends also facilitate integration with high-level programming languages, allowing SMT-solving capabilities to be seamlessly embedded into applications and formal verification processes.

The two solver frontends closest to SMT.ML in spirit are PySMT [34] and Smt-Switch [46]. They are written in Python and C++, respectively, and equip users with a high-level API for interacting with various SMT solvers, abstracting the low-level solver-specific details. PySMT supports five solvers, while Smt-Switch supports six. In particular, PySMT supports Z3, cvc5, Yices2 [27], Boolector [51], and MathSAT5 [21], whereas Smt-Switch supports Z3, cvc5, Yices2, MathSAT5, Bitwuzla, and Boolector. PySMT further implements a solver-agnostic optimisation layer that uses a set of simplification rules not unlike ours before passing the formulas to the solvers. However, SMT.ML is the only SMT frontend that includes a simplification-management engine for streamlining the integration of new simplifications and the verification of their correctness. Furthermore, unlike PySMT and Smt-Switch, SMT.ML also includes an integrated caching system, which is essential for containing memory consumption in our target applications and further improves performance. Finally, as the first SMT-solver frontend for OCaml, SMT.ML makes it easier for program analysis and verification tools implemented in OCaml (e.g., Frama-C [23], Binsec [25], Gillian [31]) to interface with multiple SMT solvers and take advantage of its optimisations.

*Caching and Simplifications for SMT Solvers.* While most SMT solvers apply some built-in simplifications as part of a preprocessing step before the core satisfiability check [6, 26], the practical scope of these simplifications is often limited. For this reason, prior work has explored augmenting SMT workflows with additional simplification capabilities by either integrating them into the solver code-

base [52] or applying them as a preprocessing stage before invoking the solver [31, 35, 45]. Empirical evidence shows that such simplifications are important for tool performance: for example, the verification of the real-world AWS code done in [45] would not have been tractable without simplifications. Our contribution differs from these systems in that we are the first to propose a declarative DSL for specifying simplifications, from which we generate both their implementations and proof skeletons. This design streamlines the management and verification of simplifications, keeping them out of the trusted computing base.

The primary SMT application that benefits from formula caching is symbolic execution [5, 17]. Symbolic execution of a program produces path formulas that record the branch conditions encountered during execution; these formulas often contain redundancies across different executions and even within the same one. For this reason, many high-profile symbolic execution tools implement formula caching [16, 54, 59], often reimplementing similar code. To address this, the Green system was proposed to factor formula caching out of individual symbolic execution tools so that different tools can share a common cache [64]. We follow the same philosophy in SMT.ML, shifting the responsibility for caching from SMT clients to SMT.ML. Unlike Green, however, we combine formula caching with hash-consing to reduce memory footprint.

## 7 Conclusions

We presented SMT.ML, a novel OCaml frontend for multiple SMT solvers. In contrast to existing frontends for SMT solvers in other languages, SMT.ML incorporates a solver-agnostic caching system and introduces a simplification-management engine that allows users to specify formula simplifications and prove their correctness. Our evaluation shows that SMT.ML’s maintains consistent behaviour with its backend SMT solvers and introduces no significant overhead. We further demonstrate that, for formulas generated from the symbolic execution of programs, SMT.ML’s optimisations are highly effective, leading to significant performance improvements.

In future, we plan to explore a number of pathways for advancing the work on SMT.ML. Firstly, we will continue to add support for new solver backends and also offer developers the option to create solver portfolios parameterised on solver selection strategies. Moreover, we plan to improve our simplification module by adding further, more complex, simplifications, based on those currently present in state-of-the-art symbolic executors [31, 58]. Finally, we will also investigate the option of integrating large language models with SMT.ML, with the aim of automating the proofs of simplification correctness.

## References

1. Ábrahám, E., Kremer, G.: SMT solving for arithmetic theories: Theory and tool support. In: 2017 19th International Symposium on Symbolic and Numeric Algorithms for Scientific Computing (SYNASC). pp. 1–8. IEEE (2017)

2. Andr  s, L., Marques, F., Carcano, A., Chambart, P., Fragoso Femenin dos Santos, J., Filli  tre, J.C.: Owi: Performant Parallel Symbolic Execution Made Easy, an Application to WebAssembly. *The Art, Science, and Engineering of Programming* **9**(2) (Oct 2024), <https://hal.science/hal-04627413>
3. Aydin, A., Bang, L., Bultan, T.: Automata-based model counting for string constraints. In: *International Conference on Computer Aided Verification*. pp. 255–272. Springer (2015)
4. Baier, D., Beyer, D., Friedberger, K.: Javasm 3: Interacting with smt solvers in java. In: *International Conference on Computer Aided Verification*. pp. 195–208. Springer (2021)
5. Baldoni, R., Copp  , E., D’elia, D.C., Demetrescu, C., Finocchi, I.: A survey of symbolic execution techniques. *ACM Computing Surveys (CSUR)* **51**(3), 1–39 (2018)
6. Barbosa, H., Barrett, C., Brain, M., Kremer, G., Lachnitt, H., Mann, M., Mohamed, A., Mohamed, M., Niemetz, A., N  tzli, A., et al.: cvc5: A versatile and industrial-strength SMT solver. In: *International Conference on Tools and Algorithms for the Construction and Analysis of Systems*. pp. 415–442. Springer (2022)
7. Barnett, M., Chang, B.Y.E., DeLine, R., Jacobs, B., Leino, K.R.M.: Boogie: A modular reusable verifier for object-oriented programs. In: *Formal Methods for Components and Objects: 4th International Symposium, FMCO 2005, Amsterdam, The Netherlands, November 1-4, 2005, Revised Lectures 4*. pp. 364–387. Springer (2006)
8. Barrett, C., Conway, C.L., Deters, M., Hadarean, L., Jovanovi  , D., King, T., Reynolds, A., Tinelli, C.: Cvc4. In: *Computer Aided Verification: 23rd International Conference, CAV 2011, Snowbird, UT, USA, July 14-20, 2011. Proceedings 23*. pp. 171–177. Springer (2011)
9. Barrett, C., Stump, A., Tinelli, C., et al.: The SMT-LIB standard: Version 2.0. In: *Proceedings of the 8th international workshop on satisfiability modulo theories (Edinburgh, UK)*. vol. 13, p. 14 (2010)
10. Beyer, D.: Software testing: 5th comparative evaluation: Test-Comp 2023. *Fundamental Approaches to Software Engineering LNCS 13991* p. 309 (2023)
11. Bj  rner, N., Levatich, M., Lopes, N.P., Rybalchenko, A., Vupp  lapati, C.: Supercharging Plant Configurations Using Z3. In: *Proc. of the 18th International Conference on Integration of Constraint Programming, Artificial Intelligence, and Operations Research (CPAIOR) (Jul 2021)*. [https://doi.org/10.1007/978-3-030-78230-6\\_1](https://doi.org/10.1007/978-3-030-78230-6_1)
12. Bobot, F., Marre, B., Bury, G., Graham-Lengrand, S., Ait El Hara, H.R.: Colibri2. webpage: <https://colibri.frama-c.com>, source code: <https://git.frama-c.com/pub/colibrics>
13. Brillout, A., Kroening, D., R  mmer, P., Wahl, T.: An interpolating sequent calculus for quantifier-free presburger arithmetic. *Journal of Automated Reasoning* **47**(4), 341–367 (2011)
14. Bromberger, M., Bobot, F., , et al.: The International Satisfiability Modulo Theories Competition (SMT-COMP)(2024). <https://smt-comp.github.io/>
15. Bryce, D., Gao, S., Musliner, D., Goldman, R.: SMT-based nonlinear PDDL+ planning. In: *Proceedings of the AAAI Conference on Artificial Intelligence*. vol. 29 (2015)
16. Cadar, C., Dunbar, D., Engler, D.R., et al.: KLEE: unassisted and automatic generation of high-coverage tests for complex systems programs. In: *OSDI*. vol. 8, pp. 209–224 (2008)
17. Cadar, C., Sen, K.: Symbolic execution for software testing: three decades later. *Communications of the ACM* **56**(2), 82–90 (2013)

18. Cha, S.K., Aygerinos, T., Rebert, A., Brumley, D.: Unleashing Mayhem on Binary Code. In: 2012 IEEE Symposium on Security and Privacy. pp. 380–394 (2012). <https://doi.org/10.1109/SP.2012.31>
19. Champion, A.: rsmt2. webpage: <https://docs.rs/rsmt2/latest/rsmt2/index.html>, source code: <https://github.com/kino-mc/rsmt2>
20. Christ, J., Hoenicke, J., Nutz, A.: Smtinterpol: An interpolating smt solver. In: International SPIN Workshop on Model Checking of Software. pp. 248–254. Springer (2012)
21. Cimatti, A., Griggio, A., Schaafsma, B.J., Sebastiani, R.: The mathsat5 SMT solver. In: Piterman, N., Smolka, S.A. (eds.) Tools and Algorithms for the Construction and Analysis of Systems - 19th International Conference, TACAS 2013, Held as Part of the European Joint Conferences on Theory and Practice of Software, ETAPS 2013, Rome, Italy, March 16–24, 2013. Proceedings. Lecture Notes in Computer Science, vol. 7795, pp. 93–107. Springer (2013). [https://doi.org/10.1007/978-3-642-36742-7\\_7](https://doi.org/10.1007/978-3-642-36742-7_7), [https://doi.org/10.1007/978-3-642-36742-7\\_7](https://doi.org/10.1007/978-3-642-36742-7_7)
22. Conchon, S., Coquereau, A., Iguernlala, M., Mebsout, A.: Alt-Ergo 2.2. In: SMT Workshop: International Workshop on Satisfiability Modulo Theories (2018)
23. Cuoq, P., Kirchner, F., Kosmatov, N., Prevosto, V., Signoles, J., Yakobowski, B.: Frama-c: A software analysis perspective. In: International conference on software engineering and formal methods. pp. 233–247. Springer (2012)
24. Damm, W., Hermanns, H.: Computer Aided Verification: 19th International Conference, CAV 2007, Berlin, Germany, July 3–7, 2007, Proceedings, vol. 4590. Springer (2007)
25. Daniel, L.A., Bardin, S., Rezk, T.: Binsec/rel: Efficient relational symbolic execution for constant-time at binary-level. In: 2020 IEEE Symposium on Security and Privacy (SP). pp. 1021–1038. IEEE (2020)
26. De Moura, L., Bjørner, N.: Z3: An efficient SMT solver. In: International conference on Tools and Algorithms for the Construction and Analysis of Systems. pp. 337–340. Springer (2008)
27. Dutertre, B.: Yices 2.2. In: International Conference on Computer Aided Verification. pp. 737–744. Springer (2014)
28. Erkok, L.: SBV. webpage: <https://hackage.haskell.org/package/sbv>, source code: <https://github.com/LeventErkok/sbv>
29. Filliâtre, J.C., Conchon, S.: Type-safe modular hash-consing. In: Proceedings of the 2006 Workshop on ML. p. 12–19. ML '06, Association for Computing Machinery, New York, NY, USA (2006). <https://doi.org/10.1145/1159876.1159880>, <https://doi.org/10.1145/1159876.1159880>
30. Filliâtre, J.C., Paskevich, A.: Why3—where programs meet provers. In: Programming Languages and Systems: 22nd European Symposium on Programming, ESOP 2013, Held as Part of the European Joint Conferences on Theory and Practice of Software, ETAPS 2013, Rome, Italy, March 16–24, 2013. Proceedings 22. pp. 125–128. Springer (2013)
31. Fragoso Santos, J., Maksimović, P., Ayoun, S.É., Gardner, P.: Gillian, part i: a multi-language platform for symbolic execution. In: Proceedings of the 41st ACM SIGPLAN Conference on Programming Language Design and Implementation. pp. 927–942 (2020)
32. Ganesh, V., Dill, D.L.: A decision procedure for bit-vectors and arrays. In: International conference on computer aided verification. pp. 519–531. Springer (2007)

33. Gao, S., Kong, S., Clarke, E.M.: *dreal*: An smt solver for nonlinear theories over the reals. In: International conference on automated deduction. pp. 208–214. Springer (2013)
34. Gario, M., Micheli, A.: *PySMT*: a solver-agnostic library for fast prototyping of SMT-based algorithms. In: SMT workshop. vol. 2015 (2015)
35. Grannan, Z., Vazou, N., Darulova, E., Summers, A.J.: *Rest*: Integrating term rewriting with program verification (extended version). arXiv preprint arXiv:2202.05872 (2022)
36. Guilloud, S., Bucev, M., Milovančević, D., Kunčák, V.: Formula normalizations in verification. In: International Conference on Computer Aided Verification. pp. 398–422. Springer (2023)
37. Hyvärinen, A.E., Marescotti, M., Alt, L., Sharygina, N.: *Opensmt2*: An smt solver for multi-core and cloud computing. In: International Conference on Theory and Applications of Satisfiability Testing. pp. 547–553. Springer (2016)
38. IEEE: IEEE Standard for Floating-Point Arithmetic. IEEE Std 754-2019 (Revision of IEEE 754-2008) (2019)
39. Inc., G.: *What4*. source code: <https://github.com/GaloisInc/what4>
40. Jacobs, B., Smans, J., Philippaerts, P., Vogels, F., Penninckx, W., Piessens, F.: *VeriFast*: A powerful, sound, predictable, fast verifier for C and Java. In: NASA formal methods symposium. pp. 41–55. Springer (2011)
41. Lee, J., Kim, D., Hur, C.K., Lopes, N.P.: An SMT encoding of LLVM’s memory model for bounded translation validation. In: Proc. of the 33rd International Conference on Computer-Aided Verification (CAV) (Jul 2021). [https://doi.org/10.1007/978-3-030-81688-9\\_35](https://doi.org/10.1007/978-3-030-81688-9_35)
42. Leino, K.R.M., Wüstholtz, V.: The *Dafny* integrated development environment. arXiv preprint arXiv:1404.6602 (2014)
43. Liang, T., Reynolds, A., Tsiskaridze, N., Tinelli, C., Barrett, C., Deters, M.: An efficient SMT solver for string constraints. *Formal Methods in System Design* **48**, 206–234 (2016)
44. Madeira Pereira, J., Marques, F.: *cvc5* OCaml bindings. source code: <https://github.com/formalsec/ocaml-cvc5>
45. Maksimovic, P., Ayoun, S., Santos, J.F., Gardner, P.: Gillian, part II: real-world verification for javascript and C. In: Computer Aided Verification - 33rd International Conference, CAV 2021, Virtual Event, July 20-23, 2021, Proceedings, Part II. Lecture Notes in Computer Science, vol. 12760, pp. 827–850. Springer (2021)
46. Mann, M., Wilson, A., Zohar, Y., Stuntz, L., Irfan, A., Brown, K., Donovan, C., Guman, A., Tinelli, C., Barrett, C.: *SMT-switch*: a solver-agnostic C++ API for SMT solving. In: International Conference on Theory and Applications of Satisfiability Testing. pp. 377–386. Springer (2021)
47. Marques, F., Fragoso Santos, J., Santos, N., Adão, P.: Concolic Execution for WebAssembly. In: 36th European Conference on Object-Oriented Programming (ECOOP 2022). Schloss Dagstuhl-Leibniz-Zentrum für Informatik (2022)
48. Minsky, Y., Madhavapeddy, A., Hickey, J.: *Real World OCaml: Functional programming for the masses*. " O’Reilly Media, Inc." (2013)
49. de Moura, L., Kong, S., Avigad, J., van Doorn, F., von Raumer, J.: The lean theorem prover (system description). In: Felty, A.P., Middeldorp, A. (eds.) Automated Deduction - CADE-25. pp. 378–388. Springer International Publishing, Cham (2015)
50. Niemetz, A., Preiner, M.: *Bitwuzla*. In: International Conference on Computer Aided Verification. pp. 3–17. Springer (2023)

51. Niemetz, A., Preiner, M., Wolf, C., Biere, A.: Btor2, btormc and boolector 3.0. In: International Conference on Computer Aided Verification. pp. 587–595. Springer (2018)
52. Nötzli, A., Reynolds, A., Barbosa, H., Niemetz, A., Preiner, M., Barrett, C., Tinelli, C.: Syntax-guided rewrite rule enumeration for smt solvers. In: International Conference on Theory and Applications of Satisfiability Testing. pp. 279–297. Springer (2019)
53. Palikareva, H., Cadar, C.: Multi-solver support in symbolic execution. In: Computer Aided Verification: 25th International Conference, CAV 2013, Saint Petersburg, Russia, July 13-19, 2013. Proceedings 25. pp. 53–68. Springer (2013)
54. Păsăreanu, C.S., Rungta, N.: Symbolic pathfinder: symbolic execution of java bytecode. In: Proceedings of the 25th IEEE/ACM International Conference on Automated Software Engineering. pp. 179–180 (2010)
55. Pimpalkhare, N., Mora, F., Polgreen, E., Seshia, S.A.: MedleySolver: online SMT algorithm selection. In: Theory and Applications of Satisfiability Testing–SAT 2021: 24th International Conference, Barcelona, Spain, July 5-9, 2021, Proceedings 24. pp. 453–470. Springer (2021)
56. Preiner, M., Schurr, H.J., Barrett, C., Fontaine, P., Niemetz, A., Tinelli, C.: SMT-LIB release 2024 (non-incremental benchmarks) (Apr 2024). <https://doi.org/10.5281/zenodo.11061097>, <https://doi.org/10.5281/zenodo.11061097>
57. Rakadjiev, E., Shimosawa, T., Mine, H., Oshima, S.: Parallel SMT solving and concurrent symbolic execution. In: 2015 IEEE Trustcom/BigDataSE/ISPA. vol. 3, pp. 17–26. IEEE (2015)
58. Roşu, G., Şerbănuţă, T.F.: An overview of the k semantic framework. The Journal of Logic and Algebraic Programming **79**(6), 397–434 (2010)
59. Ryan, K., Sturton, C.: Sylq-sv: Scaling symbolic execution of hardware designs with query caching. In: Proceedings of the 30th ACM International Conference on Architectural Support for Programming Languages and Operating Systems, Volume 3. pp. 195–211 (2025)
60. Scott, J., Niemetz, A., Preiner, M., Nejati, S., Ganesh, V.: Algorithm selection for SMT: MachSMT: machine learning driven algorithm selection for SMT solvers. International Journal on Software Tools for Technology Transfer **25**(2), 219–239 (2023)
61. Sebastiani, R., Trentin, P.: Optimathsat: A tool for optimization modulo theories. In: International conference on computer aided verification. pp. 447–454. Springer (2015)
62. Slivkins, A., et al.: Introduction to multi-armed bandits. Foundations and Trends® in Machine Learning **12**(1-2), 1–286 (2019)
63. Tuong, F., Le Fessant, F., Gazagnaire, T.: OPAM: an OCaml packa manager. In: ACM SIGPLAN OCaml Users and Developers Workshop (2012)
64. Visser, W., Geldenhuys, J., Dwyer, M.B.: Green: reducing, reusing and recycling constraints in program analysis. In: Proceedings of the ACM SIGSOFT 20th International Symposium on the Foundations of Software Engineering. pp. 1–11 (2012)
65. Vouillon, J., Balat, V.: From bytecode to javascript: the js\_of\_ocaml compiler. Software: Practice and Experience **44**(8), 951–972 (2014)
66. Wilson, A., Noetzli, A., Reynolds, A., Cook, B., Tinelli, C., Barrett, C.W.: Partitioning Strategies for Distributed SMT Solving. In: FMCAD. pp. 199–208 (2023)

## A List of Simplifications

All default simplifications used by our simplification engine are listed below, written in our DSL syntax. Each rule is shown as a simplification from the left-hand side to the right-hand side.

### A.1 Definitional Simplifications

1.  $((\text{unop\_ty Head (concrete Ty\_list l)}) \implies (\text{List.hd l}))$
2.  $((\text{unop\_ty Reverse (concrete Ty\_list l)}) \implies (\text{concrete Ty\_list (List.rev l)}))$
3.  $((\text{unop\_ty Tail (concrete Ty\_list l)}) \implies (\text{concrete Ty\_list (List.tl l)}))$
4.  $((\text{unop\_ty Length (concrete Ty\_list l)}) \implies (\text{Val (Int (List.length l))}))$
5.  $((\text{binop\_ty At (concrete Ty\_list es) (Val (Int n))}) \implies (\text{List.nth es n}))$
6.  $((\text{binop\_ty List\_cons hte1 (concrete Ty\_list l)}) \implies (\text{concrete Ty\_list (List.cons hte1 l)}))$
7.  $((\text{binop\_ty List\_append (concrete Ty\_list l0) (concrete Ty\_list l1)}) \implies (\text{concrete Ty\_list l0}))$
8.  $((\text{binop\_ty List\_append (concrete Ty\_list l1) (concrete Ty\_list l2)}) \implies (\text{concrete Ty\_list l2}))$
9.  $((\text{binop\_ty List\_append (concrete Ty\_list l0) (Val (List l1))}) \implies (\text{concrete Ty\_list (List.append l0 (List.map value l1))}))$
10.  $((\text{binop\_ty List\_append (Val (List l0)) (concrete Ty\_list l1)}) \implies (\text{concrete Ty\_list (List.append (List.map value l0) l1)}))$
11.  $((\text{binop\_ty List\_append (concrete Ty\_list l0) (concrete Ty\_list l1)}) \implies (\text{concrete Ty\_list (List.append l0 l1)}))$

### A.2 Non-Definitional Simplifications

1.  $((\text{unop\_ty Not (unop\_ty Not x)}) \implies (x))$
2.  $((\text{unop\_ty Neg (unop\_ty Neg x)}) \implies (x))$
3.  $((\text{unop\_ty Not (relop\_ty Lt e1 e2)}) \implies (\text{relop\_ty Le e2 e1}))$
4.  $((\text{unop\_ty Not (relop\_ty Le e1 e2)}) \implies (\text{relop\_ty Lt e2 e1}))$
5.  $((\text{unop\_ty Not (relop\_ty Gt e1 e2)}) \implies (\text{relop\_ty Le e1 e2}))$
6.  $((\text{unop\_ty Not (relop\_ty Ge e1 e2)}) \implies (\text{relop\_ty Lt e1 e2}))$
7.  $((\text{unop\_ty Reverse (unop\_ty Reverse l)}) \implies (l))$
8.  $((\text{binop\_ty And (Val True) hte}) \implies hte)$
9.  $((\text{binop\_ty And hte (Val True)}) \implies hte)$
10.  $((\text{binop\_ty Or (Val (Bitv 0)) hte2}) \implies hte2)$
11.  $((\text{binop\_ty Or hte1 (Val (Bitv 0))}) \implies hte1)$
12.  $((\text{binop\_ty Add (Val (Bitv 0)) hte1}) \implies hte1)$
13.  $((\text{binop\_ty Add hte1 (Val (Bitv 0))}) \implies hte1)$
14.  $((\text{binop\_ty And (Val (Bitv 0)) hte2}) \implies (\text{Val (Bitv 0)}))$
15.  $((\text{binop\_ty And hte1 (Val (Bitv 0))}) \implies (\text{Val (Bitv 0)}))$
16.  $((\text{binop\_ty Mul (Val (Bitv 0)) hte2}) \implies (\text{Val (Bitv 0)}))$

17.  $((\text{binop\_ty Mul\_hte1 (Val (Bitv 0))}) \implies (\text{Val (Bitv 0)}))$
18.  $((\text{binop\_ty Mul (Val (Bitv 1)) hte2}) \implies \text{hte2})$
19.  $((\text{binop\_ty Mul hte1 (Val (Bitv 1))}) \implies \text{hte1})$
20.  $((\text{binop ty Add (binop ty Add x (Val v1)) (Val v2)}) \implies (\text{binop ty Add x (Val (Eval.binop ty Add v1 v2))}))$
21.  $((\text{binop ty Sub (binop ty Sub x (Val v1)) (Val v2)}) \implies (\text{binop ty Sub x (Val (Eval.binop ty Sub v1 v2))}))$
22.  $((\text{binop ty Mul (binop ty Mul x (Val v1)) (Val v2)}) \implies (\text{binop ty Mul x (Val (Eval.binop ty Mul v1 v2))}))$
23.  $((\text{binop ty Add (Val v1) (binop ty Add x (Val v2))}) \implies (\text{binop ty Add (Val (Eval.binop ty Add v1 v2)) x}))$
24.  $((\text{binop ty Mul (Val v1) (binop ty Mul x (Val v2))}) \implies (\text{binop ty Mul (Val (Eval.binop ty Mul v1 v2)) x}))$
25.  $((\text{triop\_ty Ite (Val True) e1\_e2}) \implies (e1))$
26.  $((\text{triop\_ty Ite (Val False) e1 e2}) \implies (e2))$
27.  $((\text{triop\_ty Ite c1 (triop\_ty1 Ite c2 e1 e2) (triop\_ty2 Ite c3 e3 e4)}) \implies (\text{triop\_ty Ite (binop Ty\_bool And c1 c2) e1 (triop ty Ite c1 e2 (triop\_ty Ite c3 e3 e4))}))$
28.  $((\text{triop ty Extract bv (Val (Int h)) (Val (Int l))}) \implies (\text{bv})$   
 $\text{when } (h < (\text{size bv}) \ \&\& \ (l \geq 0) \ \&\& \ (l \leq h) \ \&\& \ (\text{size bv}) == (h - l))$
29.  $((\text{naryop Ty\_str Concat [(naryop Ty\_str Concat l1); (naryop Ty\_str Concat l2)]}) \implies (\text{naryop Ty\_str Concat (List.append l1 l2)}))$
30.  $((\text{naryop Ty\_str Concat [(naryop Ty\_str Concat htes); hte]}) \implies (\text{naryop Ty\_str Concat (List.append htes [hte])}))$
31.  $((\text{naryop Ty\_str Concat [hte; (naryop Ty\_str Concat htes)]}) \implies (\text{naryop Ty\_str Concat (List.cons hte htes)}))$

## B Frontends for SMT Solvers

Table 2 presents a comparison of different SMT-solver frontends, including SMT.ML, based on various features such as language, SMT-LIB2 support, optimisations, and project status and Table 3 lists the solvers by each SMT frontend.

Table 2: Comparison of different SMT-solver frontends

| Tool            | Language | SMT-LIB2 Support | Optimisations   |         | Project     |       |             |
|-----------------|----------|------------------|-----------------|---------|-------------|-------|-------------|
|                 |          |                  | Simplifications | Caching | Maintained? | Stars | Last Commit |
| SMT.ML          | OCaml    | ✓                | ✓               | ✓       | ✓           | 51    | 2025        |
| PySMT [34]      | Python   | ✓                | ✓               | ✗       | ✓           | 619   | 2025        |
| Smt-Switch [46] | C++      | ✓                | ✗               | ✗       | ✓           | 130   | 2025        |
| JavaSMT [4]     | Java     | ✗                | ✓               | ✗       | ✓           | 229   | 2025        |
| SBV [28]        | Haskell  | ✗                | ✗               | ✗       | ✓           | 261   | 2025        |
| WHAT4 [39]      | Haskell  | ✗                | ✗               | ✗       | ✓           | 169   | 2025        |
| rsmt2 [19]      | Rust     | ✗                | ✗               | ✗       | ✗           | 67    | 2022        |

Table 3: Supported solvers across different SMT frontends

| Tool       | #  | Supported Solvers                                                                                                          |
|------------|----|----------------------------------------------------------------------------------------------------------------------------|
| SMT.ML     | 5  | Alt-Ergo, Bitwuzla, Colibri2, cvc5, Z3                                                                                     |
| PySMT      | 5  | Boolector, cvc5, MathSAT5, Yices2, Z3                                                                                      |
| Smt-Switch | 6  | Bitwuzla, Boolector, cvc5, MathSAT5, Yices2, Z3                                                                            |
| JavaSMT    | 11 | Bitwuzla, Boolector, CVC4 [8], cvc5, MathSAT5, OpenSMT [37], OptiMathSAT [61], Princess [13], SMTInterpol [20], Yices2, Z3 |
| SBV        | 10 | ABC [3], Bitwuzla, Boolector, CVC4, cvc5, DReal [33], MathSAT5, OpenSMT, Yices2, Z3                                        |
| WHAT4      | 9  | ABC, Bitwuzla, Boolector, CVC4, cvc5, DReal, STP [32], Yices, Z3                                                           |
| rsmt2      | 3  | CVC4, Yices2, Z3                                                                                                           |